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### 1 [Real-time emulation and visualization of large multimedia networks](#)



Mun Choon Chan, Giovanni Pacifici, Rolf Stadler

 January 1995 **Proceedings of the third ACM international conference on Multimedia**

Publisher: ACM Press

 Full text available: [htm\(15.28 KB\)](#) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

### 2 [Multihop wireless measurements: Towards large-scale mobile network emulation through spatial switching on a wireless grid](#)



Kishore Ramachandran, Sanjit Kaul, Suhas Mathur, Marco Gruteser, Ivan Seskar

 August 2005 **Proceeding of the 2005 ACM SIGCOMM workshop on Experimental approaches to wireless network design and analysis E-WIND '05**

Publisher: ACM Press

 Full text available: [pdf\(563.41 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Experimentation with large mobile networks is notoriously tedious and expensive. We present the architecture and work-in-progress implementation of the m-ORBIT testbed, a mobility emulator using spatial switching, which facilitates mobile system experiments with 802.11a/b/g wireless network interfaces. The emulator does not require any physically moving parts---it emulates mobility by switching over an array of 128 spatially distributed radios. Instead of using hardware antenna switches, we impl ...

**Keywords:** mobility emulation, noise injection, spatial switching, testbed

### 3 [Wireless and Mobile Networks Performance: EMWIN: emulating a mobile wireless network using a wired network](#)



Pei Zheng, Lionel M. Ni

 September 2002 **Proceedings of the 5th ACM international workshop on Wireless mobile multimedia**

Publisher: ACM Press

 Full text available: [pdf\(620.08 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Test and performance evaluation of protocols and algorithms in mobile wireless networks is known to be challenging. Due to the highly varying characteristics of mobile wireless networks, one cannot merely rely on either network simulation or a testbed. Network


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# 1 [Simulation: A system for simulation, emulation, and deployment of heterogeneous sensor networks](#)

Lewis Girod, Thanos Stathopoulos, Nithya Ramanathan, Jeremy Elson, Deborah Estrin, Eric Osterweil, Tom Schoellhammer

 November 2004 **Proceedings of the 2nd international conference on Embedded networked sensor systems**

Publisher: ACM Press

 Full text available: [pdf\(345.48 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Recently deployed Wireless Sensor Network systems (WSNs) are increasingly following *heterogeneous* designs, incorporating a mixture of elements with widely varying capabilities. The development and deployment of WSNs rides heavily on the availability of simulation, emulation, visualization and analysis support. In this work, we develop tools specifically to support *heterogeneous* systems, as well as to support the measurement and visualization of *operational* ...

**Keywords:** EmStar, TinyOS, real code simulation, sensor networks

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Kishore Ramachandran, Sanjit Kaul, Suhas Mathur, Marco Gruteser, Ivan Seskar

 August 2005 **Proceeding of the 2005 ACM SIGCOMM workshop on Experimental approaches to wireless network design and analysis E-WIND '05**

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**Keywords:** mobility emulation, noise injection, spatial switching, testbed


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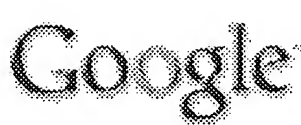
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### Babel

DEM Demonstration (file name extension) DEN Directory Enabled **Network** + Document ...  
Enhanced Host Controller **Interface** EHLLAPI **Emulator** High Level Language ...  
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### PLD LINUX Rescue CD

You need at least a i586 machine with 20MB RAM to run **PLD** RescueCD. ... with strong encryption slattach 1.60-15.2 slattach - attach a **network interface** to a ...  
[rescuecd.pld-linux.org/](http://rescuecd.pld-linux.org/) - 55k - [Cached](#) - [Similar pages](#)

### VERA -- Virtual Entity of Relevant Acronyms

PFRAM: Polymeric Ferroelectric Random Access Memory (RAM, IC) ... PNNI: Private **Network to Network Interface** (ATM), "P-NNI". PNP: Plug 'N Play (PNP), "PnP" ...  
[www.delorie.com/gnu/docs/vera/vera\\_17.html](http://www.delorie.com/gnu/docs/vera/vera_17.html) - 37k - [Cached](#) - [Similar pages](#)

### Acronym Guide - Ciena

**PLD**, Programmable Logic Device. PLL, Phase Locked Loop ... VT-100, Terminal **Emulation** System. V-UNI, Virtual User **Network Interface** ...  
[www.ciena.com/resources/resources\\_acronymguide.htm](http://www.ciena.com/resources/resources_acronymguide.htm) - 230k - [Cached](#) - [Similar pages](#)

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... ATAPI - Advanced Technology Attachment **Packet Interface** ... MANET - Mobile Ad-Hoc **Network**; MAPI - Messaging Application Programming **Interface** ...  
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### A ABI ACK ACPI ADSL ADVENT AI ALSA ANSI (American National ...

... Embedded Debian **Emulation** Enlightenment Environment Environment variable ...  
netstat **network interface** card **network** operations centre (NOC) **network** ...  
[www.lugir.org/docs/linux-word-list](http://www.lugir.org/docs/linux-word-list) - 29k - [Cached](#) - [Similar pages](#)

### P - R

P3P Platform for Privacy Preferences Project PAB Personal **Address** Book PABX ... PNG  
Portable **Network** Graphics PNNI Private **Network-to-Network Interface** PNP ...  
[www.msc-inc.net/Documents/Babel\\_Sections/Babel\\_P-R.htm](http://www.msc-inc.net/Documents/Babel_Sections/Babel_P-R.htm) - 33k - [Cached](#) - [Similar pages](#)

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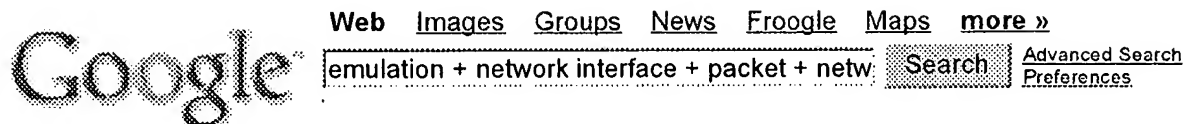
MAC - Media Access Control; MAN - Metropolitan Area **Network** ... var - variable; VAX - Virtual **Address** eXtension; VCPI - Virtual Control Program **Interface** ...  
[www.answers.com/topic/list-of-computing-and-it-abbreviations](http://www.answers.com/topic/list-of-computing-and-it-abbreviations) - 222k - [Cached](#) - [Similar pages](#)

### [PDF] &RPSXWHU \$FURQ\PV DQG \$EEUHYLDWLRQV

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ATN Augmented Transition **Network**. AUI Attachment Unit **Interface**. AVI Audio Video Interleaved ... **RAM** Random Access Memory. RAS Row-**Address** Strobe ...  
[www.phoenix.com/NR/rdonlyres/A9ADBA82-76B3-443F-9EA7-163C7E47EE82/0/glossary.pdf](http://www.phoenix.com/NR/rdonlyres/A9ADBA82-76B3-443F-9EA7-163C7E47EE82/0/glossary.pdf) - [Similar pages](#)

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### CommsDesign - Test Your Next Hardware Design—in a Live Network

The **emulator** connects through a simple parallel cable to the standard PC parallel port. The **network** connects to the PC through a standard **network interface** ...  
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Computers (nodes) connected by a **network**. Fast **network interface** ... PS Program  
**Address** Send; PW Program Access Ready Wait; PR Program Fetch **Packet** Receive ...  
[bwrc.eecs.berkeley.edu/Courses/CS252/Notes/Lec19-multiproc2.ppt](http://bwrc.eecs.berkeley.edu/Courses/CS252/Notes/Lec19-multiproc2.ppt) - [Similar pages](#)

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Computers (nodes) connected by a **network**. » Fast **network interface** ... 32-bit **address**,  
256-Bit data 512K Bits **RAM**. Host. Port. **Interface**. 2 Timers. 2 Multi- ...  
[bwrc.eecs.berkeley.edu/Courses/CS252/Notes/Microsoft%20PowerPoint%20-%20Lec19-multiproc2.pdf](http://bwrc.eecs.berkeley.edu/Courses/CS252/Notes/Microsoft%20PowerPoint%20-%20Lec19-multiproc2.pdf) - [Similar pages](#)

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The initial paper on the Desk Area **Network** may be found in the paper by Mark ...  
**Transmitter/Receiver Interface**; TBUF: Tristate Buffer (on a Xilinx **FPGA**) ...  
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(Gigabit. Ethernet. etc.) AC/DC. Power. Antenna. **Packet**. Switched ...  
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mented a hardware **network emulator** on an **FPGA**-based custom-design PCI plat- ... **MAC**  
**interface**. The **Packet** Data Receiver maintains two queues—one for storing ...  
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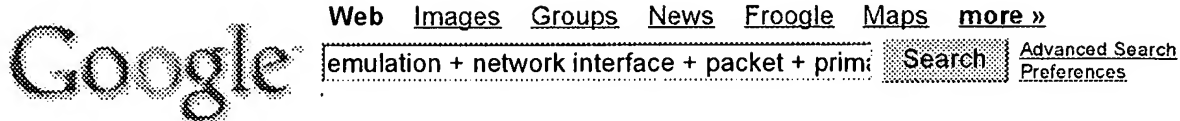
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towards definitely higher capacity and per-port **packet**. processing speed. ... match  
operation or exact **address** match on large **network**. routing tables. ...  
[www.idosoc.com/soc/pdf/cicc2000.pdf](http://www.idosoc.com/soc/pdf/cicc2000.pdf) - [Similar pages](#)

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### Acronym Guide - Ciena

PIF, **Packet Interface FPGA**. PIN, Photo Intrinsic; Positive-Intrinsic Negative; ... VT-100, Terminal Emulation System. V-UNI, Virtual User Network Interface ...  
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### Communications Technology

CCSNIS Common Channel Signaling Network Interface Specification ... MSYNC Master Synchronization - **Clock Signal**. MT Media Termination ...  
[www.ct-magazine.com/archives/ct/0602/0602\\_acronyms.html](http://www.ct-magazine.com/archives/ct/0602/0602_acronyms.html) - 101k - [Cached](#) - [Similar pages](#)

### Telewest Business :: Glossary

ATM offers fast **packet** switching for efficient use of **network** resources. ... Synchronisation is achieved by sending a **clock signal** along with the data, ...  
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### ATK-lyhenteet - Computer Oriented Abbreviations in Finnish ...

First Class VA 1) Virtual Address 2) Visual Age VAD Value Added Dealer VADD ... In-building Network WINE WINdows Emulator Win2k microsoft Windows 2000 WINS ...  
[users.utu.fi/tphleh/at\\_k\\_lyhenteet.html](http://users.utu.fi/tphleh/at_k_lyhenteet.html) - 154k - [Cached](#) - [Similar pages](#)

### Cisco IOS System Error Messages, Volume 2 of 2 - Glossary of ...

PLD. programmable logic device. PLU. **primary** logical unit. PM. Undefined, refers to SPE state machine. PNNI. Private Network-Network Interface. PoS. **Packet** ...  
[www.cisco.com/en/US/products/sw/iosswrel/ps1835/products\\_system\\_message\\_guide\\_chapter09186a008009e78b.html](http://www.cisco.com/en/US/products/sw/iosswrel/ps1835/products_system_message_guide_chapter09186a008009e78b.html) - 145k - [Cached](#) - [Similar pages](#)

### Cisco uBR7200 Series - Cisco IOS Release 12.2 CX

The National **Clock** Card allows the Cisco uBR7246VXR to accept a **primary** or ... MC16E cable **interface** line cards provide connection to an HFC **network** using ...  
[www.cisco.com/univercd/cc/td/doc/product/software/ios122/122reint/ubr7200/u72122cx.htm](http://www.cisco.com/univercd/cc/td/doc/product/software/ios122/122reint/ubr7200/u72122cx.htm) - 321k - [Cached](#) - [Similar pages](#)

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This **interface** consists of the system reset **signal**, sys\_reset\_n, and the system **clock** ... Source synchronous 250 MHz **clock** (to **FPGA**) for received data ...  
[www.xilinx.com/bvdocs/ipcenter/data\\_sheet/pcie\\_pipe\\_ds321.pdf](http://www.xilinx.com/bvdocs/ipcenter/data_sheet/pcie_pipe_ds321.pdf) - [Similar pages](#)

### COMPUTER-RELATED ACRONYMS

NAK Negatively Acknowledge NAL NetWare Application Launcher NAP **Network** ... SPX Sequenced **Packet** eXchange SQL Structured Query Language SRAM Static **RAM** ...